

DDR3 DIMM, 1.00mm PITCH, 240 CKTS, VERTICAL PRESSFIT, LOWLLCR

1.0 SCOPE

This Product Specification covers the 1.00 mm centerline gold plated DDR3 DIMM connector with vertical compliant termination to mate with 1.27 ± 0.10 mm thick memory modules.

2.0 PRODUCT DESCRIPTION

2.1 PRODUCT NAME AND SERIES NUMBER (S)

Series Number 78443

10445

Product Descriptions DDR3 DIMM, 1.00MM PITCH, 240 CKTS, VERT PRESSFIT, LOWLLCR

2.2 DIMENSIONS, MATERIALS, PLATINGS AND MARKINGS

See the appropriate Sales Drawings for information on dimensions, materials, plating and markings, recommended module outlines and footprint Specifications.

2.3 SAFETY AGENCY APPROVALS





3.0 APPLICABLE DOCUMENTS AND SPECIFICATIONS

The following documents are part of this specification between the requirements of this specified herewith. In the event of conflict between the requirements of this specification and the product drawings, the product drawings shall take precedence. In the event of conflict between the requirements of this specification and reference documents, this specification shall take precedence.

4.0 RATINGS

4.1 VOLTAGE 30 Volts AC (RMS) / DC

4.2 CURRENT

1.0 Amps / Pin

4.3 FIELD LIFE AND TEMPERATURE

Field Life:7 yearsField Temperature:65°C

4.4 TEMPERTURE

Operating Temperature: -55°C ~ +85°C **Non Operating Temperature:** -55°C ~ +85°C

5.0 PERFORMANCE

5.1 ELECTRICAL REQUIREMENTS

ITEM	DESCRIPTION	TEST CONDITION	REQUIREMENT
1	Contact Resistance (Low Level)	Mate connectors: apply a maximum voltage of 20 mV and a maximum current of 100 mA. (EIA-364-23)	10 m Ω Maximum.
2	Insulation Resistance	Unmate & unmount connectors: apply a voltage of 500 VDC between adjacent terminals and between terminals to ground. (EIA-364-21)	1 MΩ MINIMUM.
3	Dielectric Withstanding Voltage	Apply 500 VAC for 1 minute between adjacent terminals of an unmated connector. (EIA-364-20)	No breakdown.

REVISION:	ECR/ECN INFORMATION:	TITLE:	DDR3 DIMM		SHEET No.				
2	EC No: S2009-0308	1.00mm PITCH, 240 CKTS			2 of 8				
∠	<u>DATE:</u> 2008/06/30	VERTICAL	VERTICAL PRESSFIT,LOW LLCR						
DOCUMENT NUMBER:		CREATED / REVISED BY:	CHECKED BY:	APPR	OVED BY:				
PS-78443-001		CMTEO 2008/10/20	CG TAN 2008/11/05	SH LEN	2008/11/06				
TEMPLATE FILENAME: PRODUCT_SPEC[SIZE_A4](V.1).DOC									



5.2 MECHANICAL REQUIREMENTS

	ITEM	DESCRIPTION	TEST CONDIT	ON	R	EQUIREME	NT	
	4	Module Insertion Force (w/ Latches)	maximum thickness (1.37	EIA-364-13 Insert a steel gauge with the maximum thickness (1.37 ± 0.015mm) at a rate of 5 ± 1mm per minute.				
	5	Terminal Retention Force	Axial pullout force on the the housing at a rate of 2 per minute.	Contact	Contact: 0.30kgf Minimum.			
	6	Durability (Preconditioning)	Mate and unmated connective cycles at a maximum rate per minute prior to Enviro Tests. (EIA-364-09)	e of 10 cycles		tact Resista 0 mΩ Max		
	7	Durability	Mate and unmated connects 25 cycles at a maximum cycles per minute prior to Environmental Tests.		tact Resista 0 mΩ Max			
	8	Random Vibration	EIA-364-28 Module card, weighted 33 30.0mm card height; Frequency range: 5Hz to 5 to 20 Hz (Slope): (0.019 (0.02g ² /Hz) at 20Hz; 20 to 500 Hz (Flat): (0.02 20Hz ; Input acceleration is 3.13 Random control limit tole Duration: 10 minutes in e Z axis.	No Physical Damage Contact Resistance: ΔR: 10 mΩ Maximum No Discontinuities of 1 microsecond or longer duration.				
	9	Shock (Mechanical)	Module card, weighted 3 30.0mm card height; Profile: Trapezoidal shoc ±10%. Duration: 11ms Minimum Velocity change 170 Inches/sec, ±10%. Quantity: Three drops in directions. Total 18 drops connector. (EIA-364-27)	No Physical Damage Contact Resistance: ΔR: 10 mΩ Maximum. No Discontinuities of 1 microsecond or longer duration.				
REVISI	ON:	ECR/ECN INFORMATION:		DDR3 DIM	М		SHEET No.	
2		<u>EC No:</u> \$2009-0308		n PITCH, 2		ſS	3 of 8	
		DATE: 2008/06/30	VERTICAL					
DOCU		<u>-78443-001</u>	CREATED / REVISED BY: CMTEO 2008/10/20	<u>CHECKED</u> CG TAN 200			<u>2008/11/06</u>	
			·	TEN	IPLATE FILENA	ME: PRODUCT_SP	PEC[SIZE_A4](V.1).DO	



10	Module Rip out Force	Pull up from the center of the module with the latches closed at a rate of 25 ± 6 mm/minute.	9.1kgf (20lbs) Minimum retention force of the module in connector with no damage.
11	Reseating	Manually mate and un-mate the connector with module card for 3 cycles.	No damage.
12	Compliant pin insertion force to PCB (single)	Insert compliant pin into applicable PCB hole with minimum hole size 0.51mm at a rate of 25 ± 6mm per minute.	4.5kgf (10lbs) Maximum per pin.
13	Compliant pin retention force (single)	Pull compliant pin axially from PCB with size of the hole in 0.64mm maximum at a rate of 25 ± 6mm per minute.	0.50kgf (1.1lbs) Minimum per pin.
14	Module Un-mate Force	Pull out 1.17 thick test blade from connector with latches removed at a rate of 12.7 ± 3 mm/minute.	1.68kgf Minimum per connector or 14gf per pin pair.
15	Latch Overstress Force	Apply an actuation force on the latch at a rate of 25 ± 6 mm/minute in the fully open position.	3.5kgf (7.7lbs) Minimum force held for 10 seconds with no damage.
16	Latch Actuation Force	Apply an actuation force on the latch at a rate of 25 ± 6 mm/minute with recommended test module inserted into connector.	The force fully actuate the latch open shall be 4.5kgf (10lbs) Maximum per latch.

5.3 ENVIRONMENTAL REQUIREMENTS

ITEM	DESCRIPTION	TEST CON	IDITION	REQUIREMENT
17	Shock (Thermal)	-55 +0/-3 30 +25 ±10 5 +85 +3/-0 30	Duration (Minute) 0 5 MAXIMUM	Contact Resistance: ΔR : 10 m Ω Maximum. Appearance: No Damage.

REVISION:	ECR/ECN INFORMATION:	TITLE:	DDR3 DIMM		SHEET No.				
- /	EC No: S2009-0308	1.00mm PITCH, 240 CKTS			4 of 8				
L	DATE: 2008/06/30	VERTICAL	VERTICAL PRESSFIT, LOW LLCR						
DOCUMENT NUMBER:		CREATED / REVISED BY: CHECKED BY: APPE		APPR	OVED BY:				
PS-78443-001		CMTEO 2008/10/20	ITEO 2008/10/20 CG TAN 2008/11/05 SH LENI 2008/11/06						
TEMPLATE FILENAME: PRODUCT_SPEC[SIZE_A4](V.1).DOC									

molex®

PRODUCT SPECIFICATION

18	Temperature Life (Preconditioning)	Mate connectors; expose to: 91 hours at 105 ± 3°C. EIA-364-17	Contact Resistance: ΔR: 10 mΩ Maximum. Appearance: No Damage.
19	Temperature Life	Mate connectors; expose to: 165 hours at 105 ± 3°C. EIA-364-17	Contact Resistance: ΔR: 10 mΩ Maximum. Appearance: No Damage.
20	Temperature Rise	Mate the connectors, series 6 contacts and measure the temperature rise at the rated current of 1.0A after 4 hours.	Maximum Temperature Rise: 30 °C above ambient
21	Cyclic Temperature & Humidity	Cycle the connector between $25^{\circ}C \pm 3^{\circ}C$ at $80\% \pm 3\%$ RH and $65^{\circ}C \pm 3^{\circ}C$ at $50\% \pm 3\%$ RH. Ramp times should be 0.5 hour and dwell times should be 1 hour. Dwell times start when the temperature and humidity have stabilized within the specified levels. Perform 24 such cycles. EIA-364-31	Contact Resistance: ΔR: 10 mΩ Maximum. Appearance: No Damage.
22	Mixed Flowing Gas	EIA-364-65, Class IIA, expose unmated connector for 160hrs in MFG chamber. Expose mated (to same test module mated during temp life preconditioning) connector for 80hrs in MFG chamber.	Contact Resistance: ΔR : 10 m Ω Maximum.
23	Thermal Disturbance	Cycle the connector between $15^{\circ}C \pm 3^{\circ}C$ and $85^{\circ}C \pm 3^{\circ}C$, as measured on the part. Ramps should be a minimum of $2^{\circ}C$ per minute, and dwell times should insure that contacts reach temperature extreme for a minimum of 5 minutes. No humidity control. 10 cycles total.	Contact Resistance: ΔR : 10 m Ω Maximum.

REVISION:	ECR/ECN INFORMATION:	TITLE:		DDR3 DIMM		SHEET No.	
2	EC No: \$2009-0308		1.00mm PITCH, 240 CKTS				
L	DATE: 2008/06/30	· ·	VERTICAL	PRESSFIT,LOW	LLCR	5 of 8	
DOCUMEN	T NUMBER:	CREATED	/ REVISED BY:	CHECKED BY:	APPR	OVED BY:	
PS	6-78443-001	СМТЕО	2008/10/20	CG TAN 2008/11/05	SH LENI	2008/11/06	
TEMPLATE FILENAME: PRODUCT_SPEC[SIZE_A4](V.1).DOC							



Test Description		Test Group										
Sequence	1	2	3	4	5	6	7	8	9	10	11	12
Initial Contact Resistance	1	1	1		1							1
Durability (Preconditioning)	2	2	2									2
Durability					2							
Insulation Resistance				1 5								
Dielectric Withstand Voltage				2 6								
Contact Resistance	4 6	4 6 8	3 5 7		3							4, 6, 8, 10, 12
Temperature Life (Preconditioning)												3
Temperature Life	3											
Thermal Shock		3		3								
Thermal Disturbance												9
Cyclic Temp & Humidity		5		4								
Mixed Flowing Gas (unmated condition)												5
Mixed Flowing Gas (mated condition)												7
Mechanical Shock			6									
Random Vibration			4									
Reseating	5	7										11
Temperature Rise						1						
Module Insertion Force							1					
Latch Actuation Force										1		
Latch Overstress Force										2		
Module Rip out Force							2					
Compliant pin Insertion force to PCB								1				
Compliant pin Retention force to PCB								2				
Contact Retention									1			
Module Un-mate Force	1										1	
Sample Size per Test Group	5	5	5	5	5	5	5	5	5	5	5	5
		- 	г.			-						
EVISION: ECR/ECN INFORM			<u>E:</u>		1 00					(TP		<u>SHEET</u>
2												6 of
<u>DATE:</u> 2008/06/3	U	0.01							T,LOV	V LLC		
DOCUMENT NUMBER:) / REV								VED BY:
PS-78443-001		С	мтес	2008	8/10/20		CGT		08/11/05		LENI	



А	Drilled Hole	Ø0.66 ± 0.01mm		
В	Copper	0.025mm Min.		
С	Tin / Lead or Tin	0.005 ~ 0.015mm		
D	Plated Thru Hole	Ø0.51 ~ 0.64mm		

PLATED THRU HOLE (TIN IMMERSION) SPECIFICATION

А	Drilled Hole	Ø0.66 ± 0.01mm		
В	Copper	0.025mm Min.		
С	OSP	0.005 ~ 0.015mm		
D	Plated Thru Hole	Ø0.51 ~ 0.64mm		

OSP HOLE SPECIFICATION

REVISION:	ECR/ECN INFORMATION:	TITLE:	DDR3 DIMM		SHEET No.			
2	EC No: \$2009-0308	1.00mm PITCH, 240 CKTS			7 of 8			
L	DATE: 2008/06/30	VERTICAL	VERTICAL PRESSFIT,LOW LLCR					
DOCUMENT NUMBER:		CREATED / REVISED BY:	CHECKED BY:	APPR	OVED BY:			
PS-78443-001		CMTEO 2008/10/20	EO 2008/10/20 CG TAN 2008/11/05 SH LENI 2008/11/06					
TEMPLATE FILENAME: PRODUCT_SPEC[SIZE_A4](V.1).DOC								



8.0 PACKAGING

Parts shall be packed in trays and protected against damage during handling, transportation and storage.

REVISION:	ECR/ECN INFORMATION:	TITLE: DDR3 DIMM			SHEET No.
2	EC No: S2009-0308	1.00mm PITCH, 240 CKTS		8 of 8	
	DATE: 2008/06/30	VERTICAL	AL PRESSFIT, LOW LLCR		0010
DOCUMENT NUMBER:		CREATED / REVISED BY:	CHECKED BY:	APPROVED BY:	
PS-78443-001		CMTEO 2008/10/20	CG TAN 2008/11/05	SH LENI 2008/11/06	
TEMPLATE FILENAME: PRODUCT_SPEC[SIZE_A4](V.1).DOC					