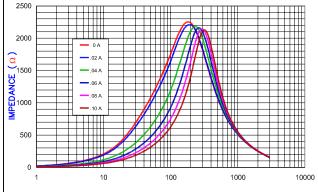
HZ0603A152R-10

UNCONTROLLED DOCUMENT

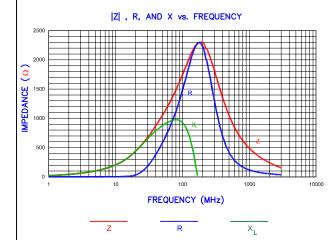
PHYSICAL DIMENSIONS:

Α	1.60	[.063]	[±] 0.15	[.006]
В	0.80	[.031]	± 0.15	[.006]
С	0.80	[.031]	± 0.15	[.006]
D	0.36	[.014]	± 0.15	[.006]

Z vs. FREQUENCY IMPEDANCE UNDER DC BIAS

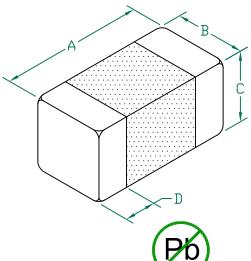


FREQUENCY (MHz)



AGILENT E4991A RF Impedance/Material Analyzer HP 16194A Test Fixture. TEST REF. 5544



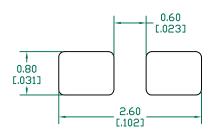


ELECT	RICAL C	HARACTERI	STICS:	
Z @ 100M (Ω)	Z @ 100MHz (<u>Ω</u>)		Rated Current	
Nominal	1500			
Minimum	1125			
Maximum	1875	0.90	100 mA	

NOTES: UNLESS OTHERWISE SPECIFIED

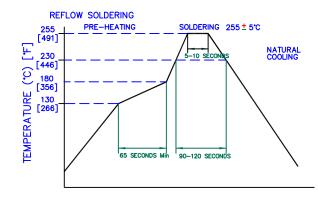
- 1. TAPED AND REELED per CURRENT EIA SPECIFICATIONS 7" REELS, 4000 PCS/REEL, PAPER CARRIER TAPE.
- 2. TERMINATION FINISH IS 100% TIN.
- 3. COMPONENTS SHOULD BE ADEQUATELY PREHEATED BEFORE SOLDERING.
- 4. OPERATEING TEMPERATURE TEMP: -40°C~+125°C (INCLUDING SELF-HEATING)

LAND PATTERNS FOR REFLOW SOLDERING



(For wave soldering, add 0.762 [.030] to this dimension)

RECOMMENDED SOLDERING CONDITIONS



I	DIMENSIONS ARE IN mm [INCHES].				This print is the property of Lair	d					
ſ					Tech. and is loaned in confidence						
ſ					subject to return upon request and with the understanding that no		'I QIFA				
Ì					copies shall be made without the written consent of Laird Tech. All rights to design or invention are reserved.		Laird				
İ											
Ì											
ŀ											
	D	ADD OPERATING TEMPERATURE	08/05/13	QU	PROJECT/PART NUMBER:	RE	EV	PART TY	PE:	DRAWN BY:	
L		ADD OPERATING TEMPERATURE UPDATE LAIRD LOGO AND REFLOW CURVE			HZ0603A152R-10		D	CO-	-FIRE	JRK	
ſ	С	UPDATE COMPANY LOGO	02/22/08	JRK	HZ0003A13ZK-10		_			ON	
Ī	В	ADD DC BIAS CURVE	07/25/06	JRK			NLE:		SHEET:		
Ī	Α	ORIGINAL DRAFT	03/29/06	JRK					2	of 2	
İ	REV	DESCRIPTION	DATE	INT	"HZ0603A152R-10-D		•	-	_	01 2	