

LTM8055

36V_{IN}, 8.5A Buck-Boost μModule® Regulator

DESCRIPTION

Demonstration circuit 2017A features the [LTM®8055](#), a buck-boost μModule® regulator that accepts input voltages lower, higher or the same as the output, but is also highly efficient due to its four-switch architecture. The output for DC2017A is 12V and the input voltage range is 5V to 36V. The maximum output current is 6A and the switching frequency is 600kHz.

DC2017A supports the adjustable and controllable features of the LTM8055 including output voltage and current regulation, switching frequency, RUN threshold, soft-start period, synchronization and reverse inductor current inhibit. In most cases, adjustment is made by modifying the appropriate resistor or capacitor component(s). DC2017A provides output current monitoring and a clock output.

Input current monitoring and regulation requires the installation of a current sense resistor. The SV_{IN} input for controller power can be made a diode-OR of power V_{IN} and the output voltage to extend the operating range of power V_{IN} to lower voltages. There are places to mount optional components that add an LC input filter and also a unity gain buffer to operate multiple DC2017As in parallel.

The LTM8055 data sheet must be read in conjunction with this demo manual to properly use or modify DC2017A.

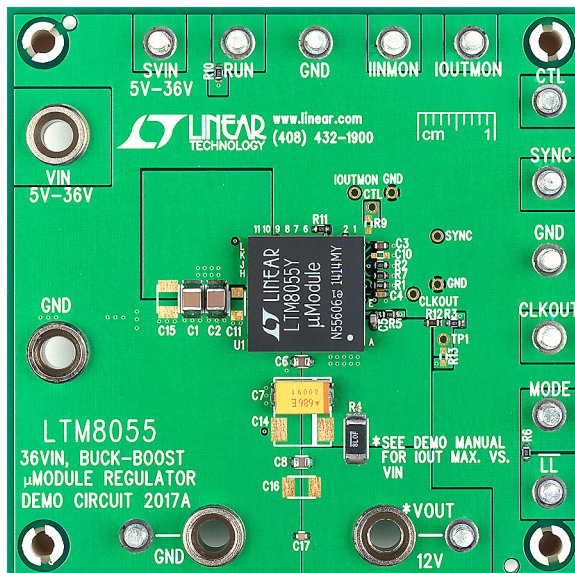
Design files for this circuit board are available at <http://www.linear.com/demo/DC2017A>

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PERFORMANCE SUMMARY Specifications are at T_A = 25°C

PARAMETER	CONDITIONS	MIN	TYP	MAX	UNITS
Minimum Input Voltage, V _{IN}				5	V
Maximum Input Voltage, V _{IN}		36			V
Maximum Output Current, I _{OUT}	12V < V _{IN} < 36V, CTL = OPEN V _{IN} = 6V, CTL = OPEN	6 3			A A
Input Turn-On Voltage, V _{IN}	R10 = 332k, R11 = 121k, V _{IN} Rising		5.5		V
Input Turn-Off Voltage, V _{IN}	R10 = 332k, R11 = 121k, V _{IN} Falling		4.5		V
Output Voltage, V _{OUT}	100mA < I _{OUT} < 6A, R2 = 11k, 1% R3 = 100k 1%, R4 = 0.008Ω	11.72		12.45	V
Efficiency	V _{IN} = 24V, I _{OUT} = 6A		94		%
Switching Frequency	R1 = 36.5k		600		kHz
Output Current Limit	R4 = 0.008Ω		6.4		A

BOARD PHOTO



QUICK START PROCEDURE

To use DC2017A to evaluate the performance of the LTM8055, refer to Figure 1 for the proper measurement equipment setup, Figure 2 for the maximum output current versus input voltage and then follow the procedure below:

NOTE: Do not hot-plug the V_{IN} terminal at high input voltages. The absolute maximum voltage on V_{IN} is 40V and hot-plugging a power supply through wire leads to the demonstration circuit can cause the voltage on the extremely low ESR ceramic input capacitor to ring to twice its DC value. In order to protect the LTM8055, an aluminum electrolytic capacitor with higher ESR is placed at the input terminals. This may protect against some, but not all, input transients due to a hot-plugged power supply. See Application Note 88 for more details.

NOTE: when measuring the input or output voltage ripple, care must be taken to avoid a long ground lead on the oscilloscope probe. Measure the input or output voltage ripple by touching the probe tip directly to terminals across

the V_{IN} or V_{OUT} capacitors. See Figure 3 for proper scope probe technique. Solder terminals near the input or output capacitors, if necessary.

1. Connect the RUN terminal to ground with a clip-on lead. Connect the power supply (with power off), load, and meters as shown in Figure 1.
2. After all connections are made, turn on the input power and verify that the input voltage is between 6V and 36V
3. Remove the clip-on lead from RUN. Verify that V_{OUT} is 12V.

NOTE: If V_{OUT} is too low, temporarily disconnect the load to make sure that the load is not set too high.

Once the proper output voltage is established, adjust the input voltage and load within the operating range and observe the output voltage regulation, ripple voltage, efficiency and other parameters.

QUICK START PROCEDURE

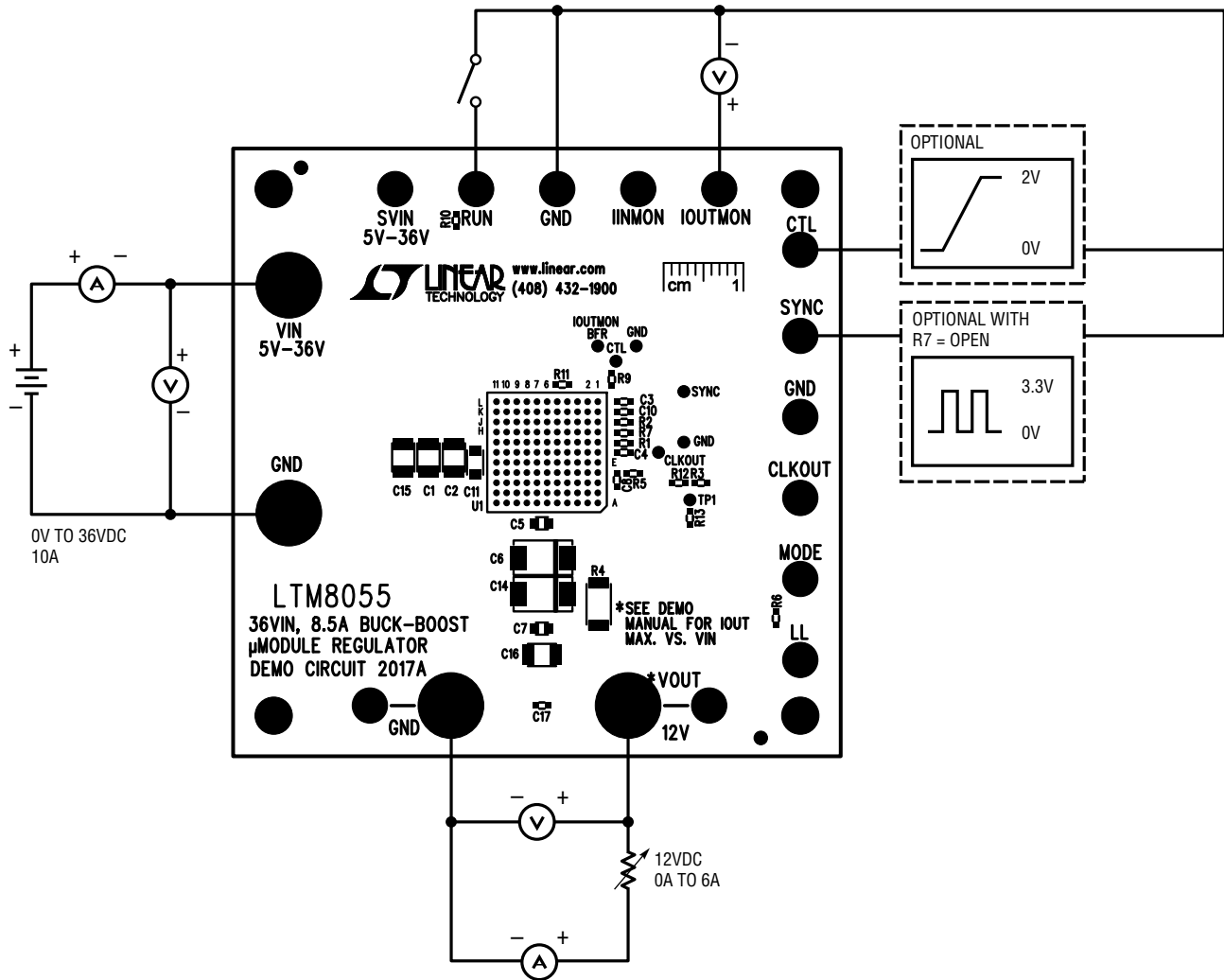


Figure 1. Proper Measurement Equipment Setup

QUICK START PROCEDURE

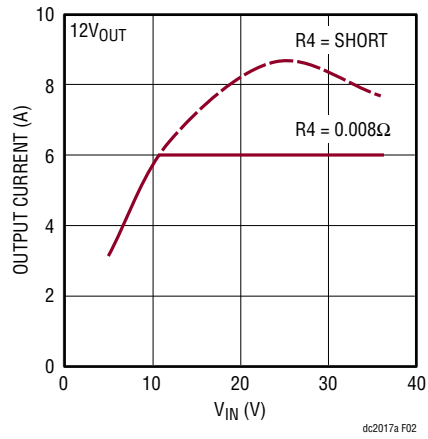


Figure 2. Maximum Output Current vs Input Voltage for V_{OUT} = 12V

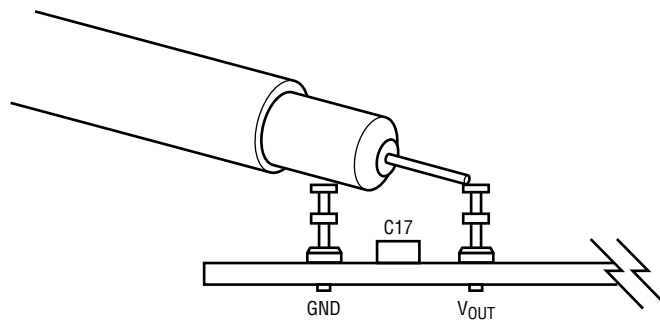


Figure 3. Proper Scope Probe Technique

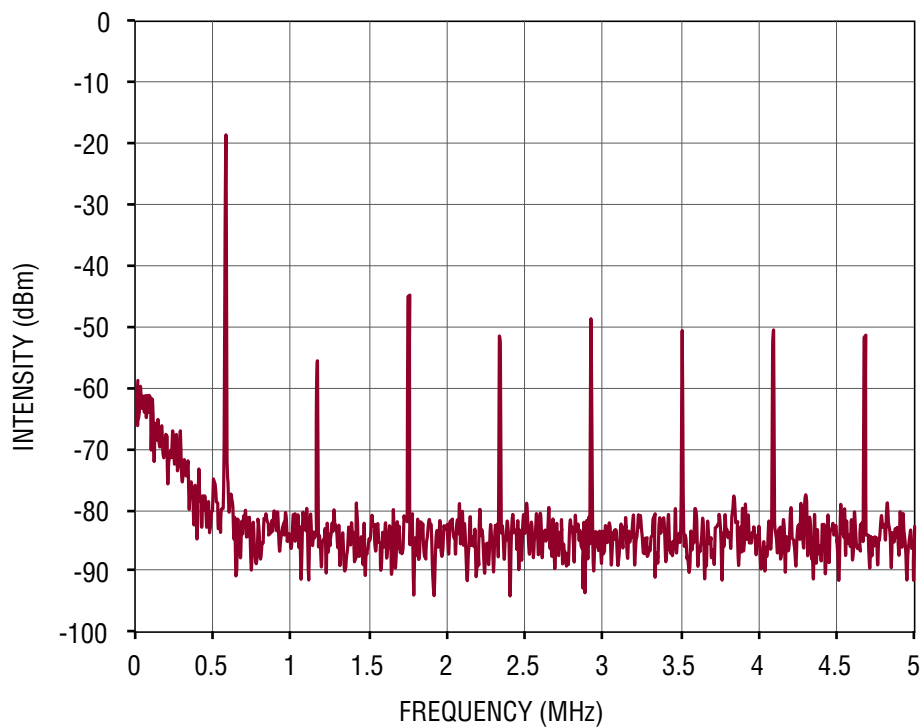
QUICK START PROCEDURE

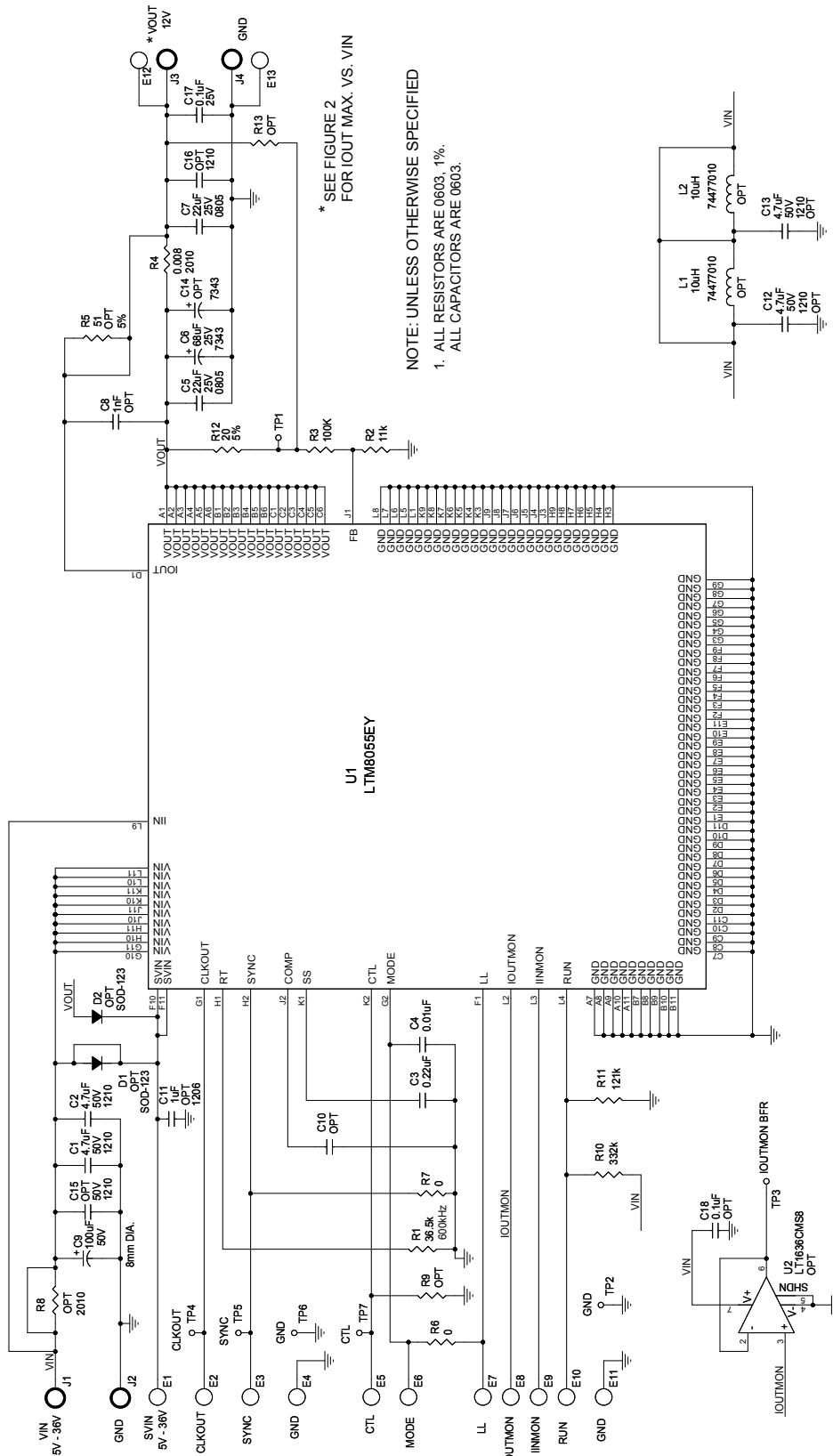
Figure 4. V_{OUT} Noise Spectrum ($V_{IN} = 24V$, $V_{OUT} = 12V$ at 6A)

DEMO MANUAL DC2017A

PARTS LIST

ITEM	QTY	REFERENCE	PART DESCRIPTION	MANUFACTURER/PART NUMBER
Required Circuit Components				
1	2	C1, C2	CAP., CER., 4.7 μ F, X7R, 50V, 10%, 1210	MURATA, GRM32ER71H475KA88L
2	1	C3	CAP., CER., 0.22 μ F, X7R, 16V, 10%, 0603	TDK, C1608X7R1C224K
3	1	C4	CAP., CER., 0.01 μ F, X7R, 16V, 10%, 0603	MURATA, GRM188R71C103KA01D
4	2	C5, C7	CAP., CER., 22 μ F, X5R, 25V, 20%, 0805	SAMSUNG, CL21A226MAQNNNE
5	1	C6	CAP., TANT., 68 μ F, 25V, 20%, 7343	AVX, TPSE686M025R0200
6	1	R1	RES., 36.5k, 1/10W, 1%, 0603	VISHAY, CRCW060336K5FKEA
7	1	R2	RES., 11k, 1/10W, 1%, 0603	VISHAY, CRCW060311K0FKEA
8	1	R3	RES., 100k, 1/10W, 1%, 0603	VISHAY, CRCW0603100KFKEA
9	1	R4	RES., SENSE, 0.008 Ω , 1/2W, 1%, 2010	VISHAY, WSL20108L000FEA
10	1	U1	I.C., 36V _{IN} Buck-Boost μ Module Regulator	LINEAR TECH., LTM8055EY#PBF
Optional Demo Circuit Components				
1	0	C8, C10, C18(OPT)	CAP., OPTION, 0603	
2	1	C9	CAP., ALUM. ELECT., 100 μ F, 50V, 8 \times 10.2	SUN ELECT., 50CE100LX
3	0	C11(OPT)	CAP., OPTION, 1206	
4	0	C12, C13, C15, C16(OPT)	CAP., OPTION, 1210	
5	0	C14(OPT)	CAP., OPTION, 7343	
6	1	C17	CAP., CER., 0.1 μ F, X7R, 25V, 10%, 0603	MURATA, GRM188R71E104KA01D
7	0	D1, D2(OPT)	DIODE, OPTION, SOD-123	
8	0	L1, L2(OPT)	IND., 10 μ H	WÜRTH, 74477010
9	0	R5, R9, R13(OPT)	RES., OPTION, 0603	
10	2	R6, R7	RES., 0 Ω , 1/10W, 0603	VISHAY, CRCW0603000Z0EA
11	0	R8(OPT)	RES., OPTION, 2010	
12	1	R10	RES., 332k, 1/10W, 1%, 0603	VISHAY, CRCW0603332KFKEA
13	1	R11	RES., 121k, 1/10W, 1%, 0603	VISHAY, CRCW0603121KFKEA
14	1	R12	RES., 20 Ω , 1/10W, 5%, 0603	VISHAY, CRCW060320R0JNEA
15	0	U2(OPT)	I.C., LT1636CMS8#PBF, 8-MSOP	LINEAR TECH., LT1636CMS8#PBF
Hardware: For Demo Board Only				
1	13	E1-E13	TESTPOINT, TURRET, .094" MTG. HOLE	MILL-MAX, 2501-2-00-80-00-00-07-0
2	4	J1, J2, J3, J4	JACK BANANA	KEYSTONE, 575-4
3	4	MH1-MH4	STANDOFF, NYLON, SNAP-ON, 0.500"	KEYSTONE, 8833

SCHEMATIC DIAGRAM



DEMO MANUAL DC2017A

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This notice contains important safety information about temperatures and voltages. For further safety concerns, please contact a LTC application engineer.

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