TOSHIBA Field Effect Transistor Silicon N Channel MOS Type (π–MOSV)

2SK2967

DC-DC Converter, Relay Drive and Motor Drive Applications

• Low drain-source ON resistance : $R_{DS}(ON) = 48 \text{ m}\Omega \text{ (typ.)}$

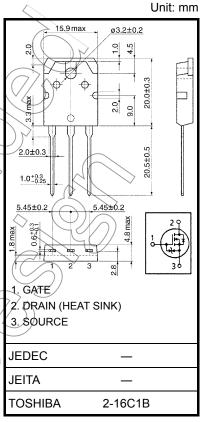
High forward transfer admittance : |Y_{fs}| = 30 S (typ.)

• Low leakage current : $I_{DSS} = 100 \mu A \text{ (max) (V}_{DS} = 250 \text{ V)}$

• Enhancement mode : $V_{th} = 1.5 \text{ to } 3.5 \text{ V } (V_{DS} = 10 \text{ V}, I_D = 1 \text{ mA})$

Absolute Maximum Ratings (Ta = 25°C)

Characteri	etice	Symbol	Rating	Unit
Characteri	Stics	Symbol	Rating	
Drain-source voltage		V_{DSS}	250	(v)
Drain-gate voltage (R	$_{\rm GS}$ = 20 k Ω)	V_{DGR}	250	V
Gate-source voltage	_	V _{GSS}	±20	V
Drain current	DC (Note 1) I _D	30	A
	Pulse (Note 1) I _{DP}	120	Α
Drain power dissipatio	n (Tc = 25°C)	PD	150	W
Single pulse avalanche	e energy (Note 2	EAS	925	mJ
Avalanche current		IAR)) 30	A
Repetitive avalanche	energy (Note 3) EAR	15 (mJ
Channel temperature		T _{ch}	150	<u></u>
Storage temperature r	ange /	Tstg	-55 to 150	(e)



Weight: 4.6 g (typ.)

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Thermal Characteristics

Characteristics	Symbol	Max	Unit
Thermal resistance, channel to case	R _{th} (ch-c)	0.833	°C/W
Thermal resistance, channel to ambient	R _{th (ch-a)}	50	°C/W

Note 1: Ensure that the channel temperature does not exceed 150°C.

Note 2: V_{DD} = 50 V, T_{ch} = 25°C (initial), L = 1.74 mH, I_{AR} = 30 A, R_G = 25 Ω

Note 3: Repetitive rating: pulse width limited by maximum channel temperature.

This transistor is an electrostatic-sensitive device.

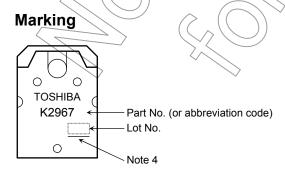
Please handle with caution.

Electrical Characteristics (Ta = 25°C)

Chara	cteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage cu	urrent	I _{GSS}	V _{GS} = ±16 V, V _{DS} = 0 V	_	_	±10	μΑ
Drain cut-off cu	rrent	I _{DSS}	V _{DS} = 250 V, V _{GS} = 0 V	_	_	100	μΑ
Drain-source bi	reakdown voltage	V (BR) DSS	I _D = 10 mA, V _{GS} = 0 V	250	_	_	V
Gate threshold	voltage	V _{th}	V _{DS} = 10 V, I _D = 1 mA	1.5	_	3.5	V
Drain-source O	N resistance	R _{DS} (ON)	V _{GS} = 10 V, I _D = 15 A	(F	48	68	mΩ
Forward transfe	r admittance	Y _{fs}	V _{DS} = 10 V, I _D = 15 A	15	30		S
Input capacitano	ce	C _{iss}		\rightarrow	5400		
Reverse transfer capacitance		C _{rss}	V _{DS} = 10 V, V _{GS} = 0 V, f = 1 MHz		580		pF
Output capacitance		Coss		_	1900		
Switching time	Rise time	t _r	V _{GS} OV I I I OVOUT		20	<i>>>></i>	
	Turn-on time	t _{on}	$RL = 6.7\Omega$	-	50	> _	20
	Fall time	t _f	v _{DD} ≒ ₁₀₀ v		35	_	ns
	Turn-off time	t _{off}	Duty ≤1%, t _w =10μs		200	_	
Total gate charge (gate-source plus gate-drain)		Qg		_	132	_	
Gate-source charge		Q _{gs}	$V_{DD} \approx 200 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 30 \text{ A}$	_	80	_	nC
Gate-drain ("miller") Charge		Q _{gd}		_	52	_	

Source-Drain Ratings and Characteristics (Ta = 25°C)

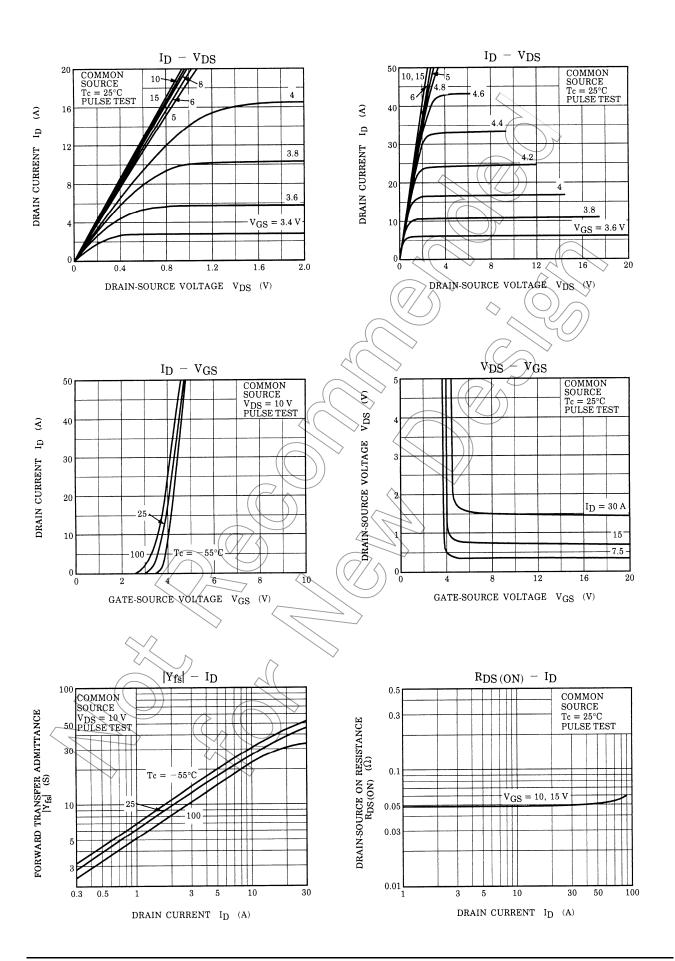
Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Continuous drain reverse current (Note 1)	IDR	((//)-	l	ı	30	Α
Pulse drain reverse current (Note 1)	I _{DRP}	_	_	_	120	Α
Forward voltage (diode)	V _{DSF}	I _{DR} = 30 A, V _{GS} = 0 V	_	_	-2.0	V
Reverse recovery time	t _{rr}	I _{DR} = 30 A, V _{GS} = 0 V		270		ns
Reverse recovery charge	Qrr	dl _{DR} / dt = 100 A / μs	_	3.0	_	μC



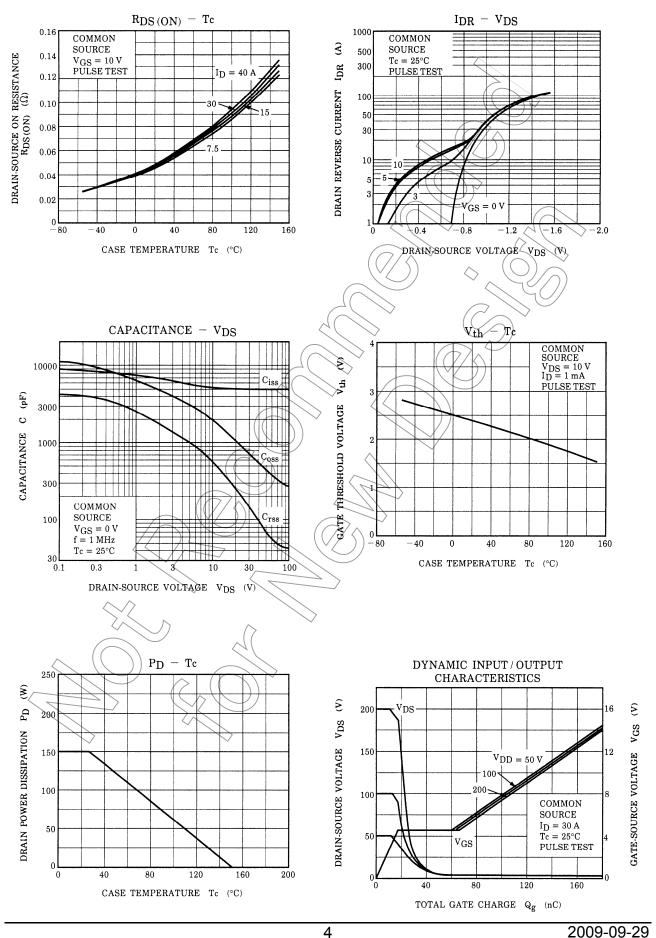
Note 4: A line under a Lot No. identifies the indication of product Labels.

Not underlined: [[Pb]]/INCLUDES > MCV Underlined: [[G]]/RoHS COMPATIBLE or [[G]]/RoHS [[Pb]]

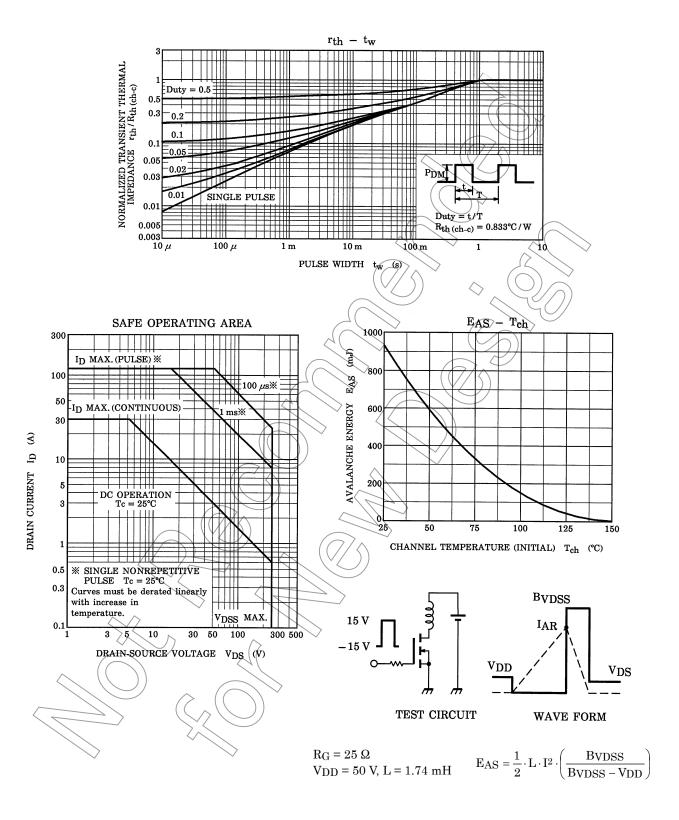
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