NST857BF3T5G

PNP General Purpose Transistor

The NST857BF3T5G device is a spin-off of our popular SOT-23/SOT-323/SOT-563/SOT-963 three-leaded device. It is designed for general purpose amplifier applications and is housed in the SOT-1123 surface mount package. This device is ideal for low-power surface mount applications where board space is at a premium.

Features

- h_{FE}, 220–475
- Low $V_{CE(sat)}$, $\leq -0.3 V$
- Reduces Board Space
- This is a Pb–Free Device

MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Collector – Emitter Voltage	V _{CEO}	-45	Vdc
Collector – Base Voltage	V _{CBO}	-50	Vdc
Emitter-Base Voltage	V _{EBO}	-5.0	Vdc
Collector Current – Continuous	۱ _C	-100	mAdc

THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Total Device Dissipation, T _A = 25°C Derate above 25°C	P _D (Note 1)	290 2.3	mW mW/°C
Thermal Resistance, Junction-to-Ambient	R _{θJA} (Note 1)	432	°C/W
Total Device Dissipation, T _A = 25°C Derate above 25°C	P _D (Note 2)	347 2.8	mW mW/°C
Thermal Resistance, Junction-to-Ambient	R _{θJA} (Note 2)	360	°C/W
Thermal Resistance, Junction-to-Lead 3	R _{ΨJL} (Note 2)	143	°C/W
Junction and Storage Temperature Range	T _J , T _{stg}	–55 to +150	°C

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

1. 100 mm² 1 oz, copper traces.

2. 500 mm² 1 oz, copper traces.



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ORDERING INFORMATION

Device	Package	Shipping [†]
NST857BF3T5G	SOT-1123 (Pb-Free)	8000/Tape & Reel

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

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ELECTRICAL CHARACTERISTICS (T_A = 25°C unless otherwise noted)

Characteristic	Symbol	Min	Тур	Max	Unit
OFF CHARACTERISTICS					
Collector – Emitter Breakdown Voltage (I _C = –10 mA)	V _{(BR)CEO}	-45	-	-	V
Collector – Emitter Breakdown Voltage (I _C = –10 μ A, V _{EB} = 0)	V _{(BR)CES}	-50	-	-	V
Collector – Base Breakdown Voltage ($I_C = -10 \ \mu A$)	V _{(BR)CBO}	-50	-	-	V
Emitter – Base Breakdown Voltage ($I_E = -1.0 \ \mu A$)	V _{(BR)EBO}	-5.0	-	-	V
Collector Cutoff Current (V _{CB} = -30 V) (V _{CB} = -30 V, T _A = 150° C)	I _{CBO}			-15 -4.0	nA μA
ON CHARACTERISTICS					•
DC Current Gain (I _C = -10 μ A, V _{CE} = -5.0 V) (I _C = -2.0 mA, V _{CE} = -5.0 V)	h _{FE}	_ 220	150 290	_ 475	-
Collector – Emitter Saturation Voltage ($I_C = -10 \text{ mA}, I_B = -0.5 \text{ mA}$) ($I_C = -100 \text{ mA}, I_B = -5.0 \text{ mA}$)	V _{CE(sat)}			-0.3 -0.7	V
Base – Emitter Saturation Voltage ($I_C = -10 \text{ mA}$, $I_B = -0.5 \text{ mA}$) ($I_C = -100 \text{ mA}$, $I_B = -5.0 \text{ mA}$)	V _{BE(sat)}		-0.7 -0.9		V
Base – Emitter On Voltage ($I_C = -2.0 \text{ mA}, V_{CE} = -5.0 \text{ V}$) ($I_C = -10 \text{ mA}, V_{CE} = -5.0 \text{ V}$)	V _{BE(on)}	-0.6 -		-0.75 -0.82	V
SMALL-SIGNAL CHARACTERISTICS					•
Current – Gain – Bandwidth Product ($I_C = -10 \text{ mA}, V_{CE} = -5.0 \text{ Vdc}, f = 100 \text{ MHz}$)	fT	100	-	-	MHz
Output Capacitance ($V_{CB} = -10 \text{ V}, \text{ f} = 1.0 \text{ MHz}$)	C _{obo}	-	_	4.5	pF
Input Capacitance (V _{EB} = -0.5 V, I _C = 0 mA, f = 1.0 MHz)	C _{ibo}	-	-	10	pF
Noise Figure (I _C = –0.2 mA, V _{CE} = –5.0 Vdc, R _S = 2.0 k Ω , f = 1.0 kHz, BW = 200 Hz)	NF	_	-	10	dB



NST857BF3T5G



PACKAGE DIMENSIONS

SOT-1123 CASE 524AA ISSUE C









NOTES:

- DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994.
 CONTROLLING DIMENSION: MILLIMETERS.
- CONTROLLING DIMENSION: MILLIMETERS.
 MAXIMUM LEAD THICKNESS INCLUDES LEAD FINISH. MINIMUM LEAD THICKNESS IS THE
- MINIMUM THICKNESS OF BASE MATERIAL
 DIMENSIONS D AND E DO NOT INCLUDE MOLD FLASH, PROTRUSIONS, OR GATE

MOL	D FLASI	1, PROI	1	
BUR	RS.		Ľ	
	MILLIMETERS			
DIM	MIN	MAX		
Α	0.34	0.40		
b	0.15	0.28		
b1	0.10	0.20		
С	0.07	0.17		
D	0.75	0.85		
E	0.55	0.65		
е	0.35	0.40		
HE	0.95	1.05		
L	0.185 REF			
L2	0.05	0.15		

STYLE 1: PIN 1. BASE 2. EMITTER 3. COLLECTOR

SOLDERING FOOTPRINT*



*For additional information on our Pb–Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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