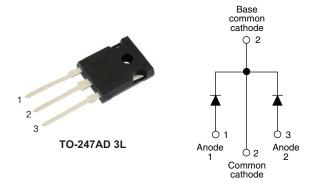


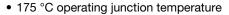
# Hyperfast Soft Recovery Diode, 2 x 15 A FRED Pt<sup>®</sup> Gen 4

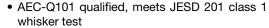


PRODUCT SUMMARY				
Package	TO-247AD 3L			
I <sub>F(AV)</sub>	2 x 15 A			
V <sub>R</sub>	600 V			
V <sub>F</sub> at I <sub>F</sub>	1.28 V			
t <sub>rr</sub> typ.	See Recovery table			
T <sub>J</sub> max.	175 °C			
Diode variation	Common cathode			

#### **FEATURES**

- Gen 4 FRED Pt® technology
- Low I<sub>RRM</sub> and reverse recovery charge
- · Very low forward voltage drop
- Polyimide passivated chip for high reliability standard











#### ROHS COMPLIANT HALOGEN FREE

### **DESCRIPTION**

Gen 4 Fred Pt technology, state of the art, ultralow  $V_F$ , soft switching optimized for Discontinuous (Critical) Mode (DCM) and IGBT F/W diode.

The minimized conduction loss, optimized stored charge and low recovery current minimize the switching losses and reduce power dissipation in the switching element and snubbers.

ABSOLUTE MAXIMUM RATINGS				
PARAMETER	SYMBOL	TEST CONDITIONS	MAX.	UNITS
Peak repetitive reverse voltage	$V_{RRM}$		600	V
Average rectified forward current	I <sub>F(AV)</sub>	T <sub>C</sub> = 142 °C	15	^
Non-repetitive peak surge current, per leg	I <sub>FSM</sub>	$T_C = 25$ °C, $t_p = 8.3$ ms half sine wave	200	A
Operating junction and storage temperature	T <sub>J</sub> , T <sub>Stg</sub>		-55 to +175	°C

<b>ELECTRICAL SPECIFICATIONS</b> (T <sub>J</sub> = 25 °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS	
Breakdown voltage, blocking voltage	$V_{BR}, V_{R}$	$I_R = 100 \mu A$	600	00			
	V <sub>F</sub>	I <sub>F</sub> = 15 A	-	1.6	1.9		
		I <sub>F</sub> = 30 A	-	1.87	-	V	
Forward voltage		I <sub>F</sub> = 15 A, T <sub>J</sub> = 125 °C	-	1.35	-		
		I <sub>F</sub> = 30 A, T <sub>J</sub> = 125 °C	-	1.67	-		
		I <sub>F</sub> = 15 A, T <sub>J</sub> = 150 °C	-	1.28	1.52		
		$I_F = 30 \text{ A}, T_J = 150  ^{\circ}\text{C}$	-	1.61	-		
Reverse leakage current	I <sub>R</sub>	$V_R = V_R$ rated	-	-	15		
		$T_J = 125$ °C, $V_R = V_R$ rated	-	-	500	μA	
Junction capacitance	C <sub>T</sub>	V <sub>R</sub> = 600 V	_	16	-	pF	



<b>DYNAMIC RECOVERY CHARACTERISTICS</b> (T <sub>J</sub> = 25 °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNITS
Boyeres reserves time		T <sub>J</sub> = 25 °C		-	50	-	20
Reverse recovery time	t <sub>rr</sub>	T <sub>J</sub> = 125 °C	I <sub>F</sub> = 15 A dI <sub>F</sub> /dt = 1000 A/μs	-	70	-	ns
Peak recovery current	1	T <sub>J</sub> = 25 °C		-	8.5	-	Α
Feak recovery current	IRRM	T <sub>J</sub> = 125 °C	$V_{\rm R} = 400 \text{ V}$	-	16	-	
Poverse receivery charge	Q <sub>rr</sub>	T <sub>J</sub> = 25 °C	VR = 400 V	-	250	-	nC
Reverse recovery charge		T <sub>J</sub> = 125 °C		-	600	-	110

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Thermal resistance, junction to case	R <sub>thJC</sub>		-	-	1.4	°C/W
Thermal resistance, case to heat sink	R <sub>thCS</sub>		-	0.4	-	
Weight			-	6.0	-	g
vveignt			-	0.21	-	OZ.
Mounting torque			6.0	_	12	kgf · cm
Mounting torque			(5)	_	(10)	(lbf $\cdot$ in)
Marking device		Case style TO-247AD 3L		C4PH3	3006LH	

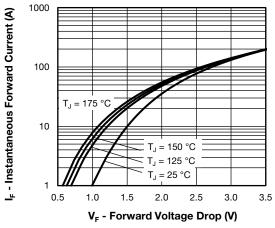


Fig. 1 - Typical Forward Voltage Drop Characteristics

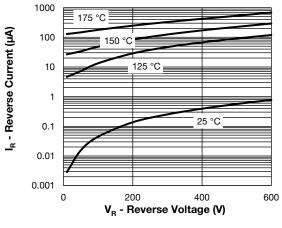


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

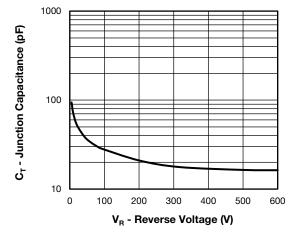


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

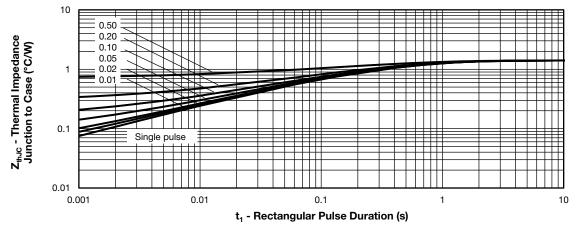


Fig. 4 - Maximum Thermal Impedance ZthJC Characteristics

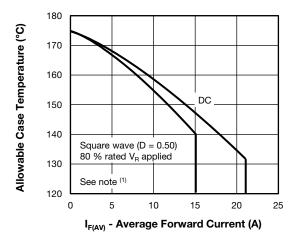


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current

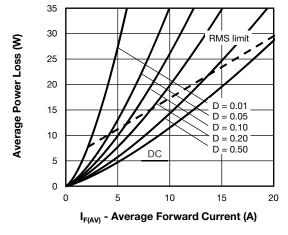


Fig. 6 - Forward Power Loss Characteristics

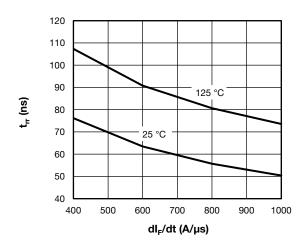


Fig. 7 - Typical Reverse Recovery Time vs.  $dI_{\text{F}}/dt$ 

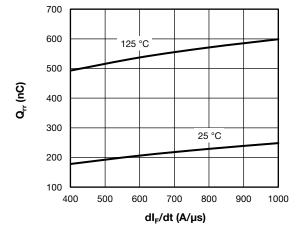


Fig. 8 - Typical Stored Charge vs. dl<sub>F</sub>/dt

#### Note

 $\begin{array}{ll} \text{(1)} \ \ \text{Formula used: } T_C = T_J - (P_d + P_{dREV}) \times R_{thJC}; \\ Pd = \text{forward power loss} = I_{F(AV)} \times V_{FM} \ \text{at } (I_{F(AV)}/D) \ \text{(see Fig. 5)} \\ P_{dREV} = \text{inverse power loss} = V_{R1} \times I_R \ \text{(1 - D); } I_R \ \text{at } V_R = \text{rated } V_R \\ \end{array}$ 

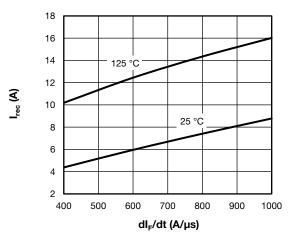


Fig. 9 - Typical Reverse Current vs. dl<sub>F</sub>/dt

#### **ORDERING INFORMATION TABLE**

**Device code** VS-C P 30 06 **N3** 4 Н Н (2) (4)(5) (6) (8) (9) (10)(3) Vishay Semiconductors product

Circuit configuration:

C = common cathode

3 - FRED Pt Gen 4

P = TO-247 package

**5** - Process type:

H = hyperfast recovery

6 - Current rating (30 = 2 x 15 A)

7 - Voltage rating (06 = 600 V)

8 - Package: L = long lead

9 - H = AEC-Q101 qualified

10 - Environmental digit:

N3 = halogen-free, RoHS-compliant, and totally lead (Pb)-free

ORDERING INFORMATION (Example)					
PREFERRED P/N QUANTITY PER TUBE MINIMUM ORDER QUANTITY PACKAGING DESCRIPTION					
VS-C4PH3006LHN3	25	500	Antistatic plastic tube		

LINKS TO RELATED DOCUMENTS					
Dimensions TO-247AD 3L <u>www.vishay.com/doc?95626</u>					
Part marking information	TO-247AD 3L	www.vishay.com/doc?95007			



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Vishay

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