Unit in mm

TOSHIBA Photocoupler GaAs Ired & Photo-Transistor

TLP181

Office Machine

Programmable Controllers

AC Adapter

I/O Interface Board

The TOSHIBA mini flat coupler TLP181 is a small outline coupler, suitable for surface mount assembly.

TLP181 consist of a photo transistor optically coupled to a gallium arsenide infrared emitting diode. Since TLP181 is smaller than DIP package, it's suitable for high-density surface mounting applications such as programmable controllers

- Collector-emitter voltage: 80V (min)
- Current transfer ratio: 50% (min)

Rank GB: 100% (min)

- Isolation voltage: 3750Vrms (min)
- Operation Temperature:-55 to 110 °C
- · Safety Standards

UL recognized: UL1577, File No. E67349

cUL recognized: CSA Component Acceptance Service No. 5A

File No.E67349

BSI approved: BS EN60065:2002, certificate No.8285
 BS EN60950-1:2006 certificate No.8286

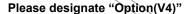
Option (V4) type

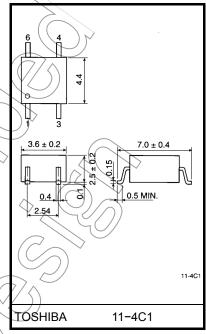
VDE approved : EN60747-5-2

Maximum Operating Insuration Voltage: 565 Vpk

Highest Permissible Overvoltage: 6000 Vpk

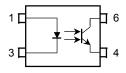
(Note): When a EN60747-5-2 approved type is needed,





Weight: 0.09 g (Typ.)

Pin Configuration (top view)

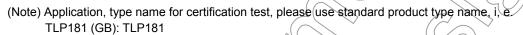


- 1: Anode
- 3: Cathode
- 4: Emitter
- 6: Collector

Current Transfer Ratio

		Current Transfer Ratio (%) (I _C / I _F)		
Туре	Classification *1	I _F = 5mA, V _{CE}	= 5V, Ta = 25°C	Marking Of Classification
		Min	Max	
	Blank	50	600	Blank ,Y,Y [■] ,YE,G,G [■] ,GR,B, B [■] ,BL,GB
	Rank Y	50	150	YE
	Rank GR	100	300	GR
	Rank BL	200	600	BL (V/)
TLP181	Rank GB	100	600	GB
	Rank YH	75	150	Y = (() >
	Rank GRL	100	200	G
	Rank GRH	150	300	G* (1)
	Rank BLL	200	400	B

^{*1:} EX, Rank GB: TLP181 (GB)





Absolute Maximum Ratings (Ta = 25°C)

	Characteristic	Symbol	Rating	Unit
	Forward current	lF	50	mA
	Forward current detating	ΔI _F / °C	−1.4 (Ta ≥89°C)	mA / °C
LED	Pulse forward current (100µs pulse, 100pps)	I _{FP}	1	A
	Reverse voltage	V _R	5	(v
	Junction temperature	Tj	125	,c)
	Collector-emitter voltage	V_{CEO}	80	// v)
	Emitter-collector voltage	V _{ECO}	7	\searrow
5	Collector current	IC	50	mA
Detector	Collector power dissipation (1 Circuit)	P _C	150	mW
	Collector power dissipation derating (1 Circuit Ta ≥ 25°C)	ΔP _C / °C	-1.5	mW/°C
	Junction temperature	Tj	125	
Sto	rage temperature range	T _{stg}	-55 to 125	°C/
Оре	erating temperature range	T _{opr}	-55 to 110	(°c)
Lead soldering temperature		T _{sol}	260 (10s)	(c)
Total package power dissipation		P _T	200	mW
Total package power dissipation derating (Ta ≥ 25°C)		ΔP _T / °C	-2.0	mW/°C
	ation voltage , 1min., R.H. ≤ 60%) (Note 1)	BVs	3750	V _{rms}

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions" ("Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

(Note 1) Device considered a two-terminal device. Pin1, 3 shorted together and pins 4, 6 shorted together

Recommended Operating Conditions

Characteristic	Symbol	Min	Тур.	Max	Unit
Supply voltage	Vcc	_	5	48	V
Forward current	l _E		16	20	mA
Collector current	Ic		1	10	mA

Note: Recommended operating conditions are given as a design guideline to obtain expected performance of the device. Additionally, each item is an independent guideline respectively. In developing designs using this product, please confirm specified characteristics shown in this document.

3

Individual Electrical Characteristics (Ta = 25°C)

	Characteristic	Symbol	Test Condition	Min	Тур.	Max	Unit
	Forward voltage	V _F	I _F = 10 mA	1.0	1.15	1.3	V
LED	Reverse current	I _R	V _R = 5 V	-	1	10	μΑ
	Capacitance	C _T	V = 0, f = 1 MHz	1	30		pF
	Collector–emitter breakdown voltage	V _(BR) CEO	I _C = 0.5 mA	80	7/		٧
<u></u>	Emitter-collector breakdown voltage	V _(BR) ECO	I _E = 0.1 mA	7	7_	_	V
Detector	Collector dark current I _{CEO}	loro	V _{CE} = 48 V, (Ambient light below 1000 lx)))	0.01 (2)	0.1 (10)	μΑ
		V _{CE} = 48 V, Ta = 85°C, (Ambient light below 1000 lx)		2 (4)	50 (50)	μΑ	
	Capacitance (collector to emitter)	C _{CE}	V = 0, f = 1 MHz	_	10	//	pF

Coupled Electrical Characteristics (Ta = 25°C)

Characteristic	Symbol	Test Condition	Mth	Typ	Max	Unit
Current transfer ratio	Ic / I _E	IF = 5 mA, VCE = 5 V	50)	_	600	%
		Rank GB	100	_	600	,,
Saturated CTR	lo / le /	IF = 1 mA, V _{CE} = 0.4 V) —	60	_	%
Saturated CTIV	IC / IF (sat)	Rank GB	30	_	_	70
		IC = 2.4 mA, IF = 8 mA	_	_	0.4	
Collector–emitter saturation voltage	VCE (sat)	I _C = 0.2 mA, I _F = 1 mA	1	0.2	_	V
		Rank GB		_	0.4	
Off-state collector current	Ic (off)	$V_F = 0.7V, V_{CE} = 48 V$		1	10	μΑ

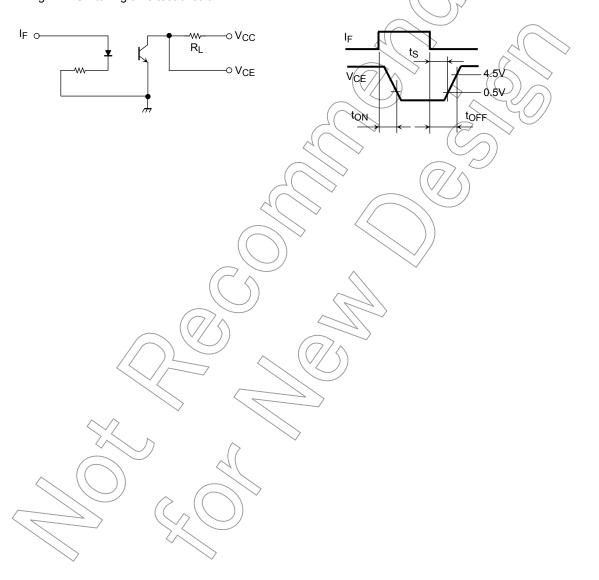
Isolation Characteristics (Ta = 25°C)

Characteristic	Symbol	Test Condition	Min	Тур.	Max	Unit
Capacitance (input to output)	CS	V _S = 0V, f = 1 MHz	-	0.8	_	pF
Isolation resistance	R _S	V _S = 500 V, R.H. ≤ 60%	1×10 ¹²	10 ¹⁴	_	Ω
	. (7	AC, 1 minute	3750	-	_	V
Isolation voltage	BVs	AC, 1 second, in oil	_	10000	_	V _{rms}
		DC, 1 minute, in oil	_	10000	_	V_{dc}

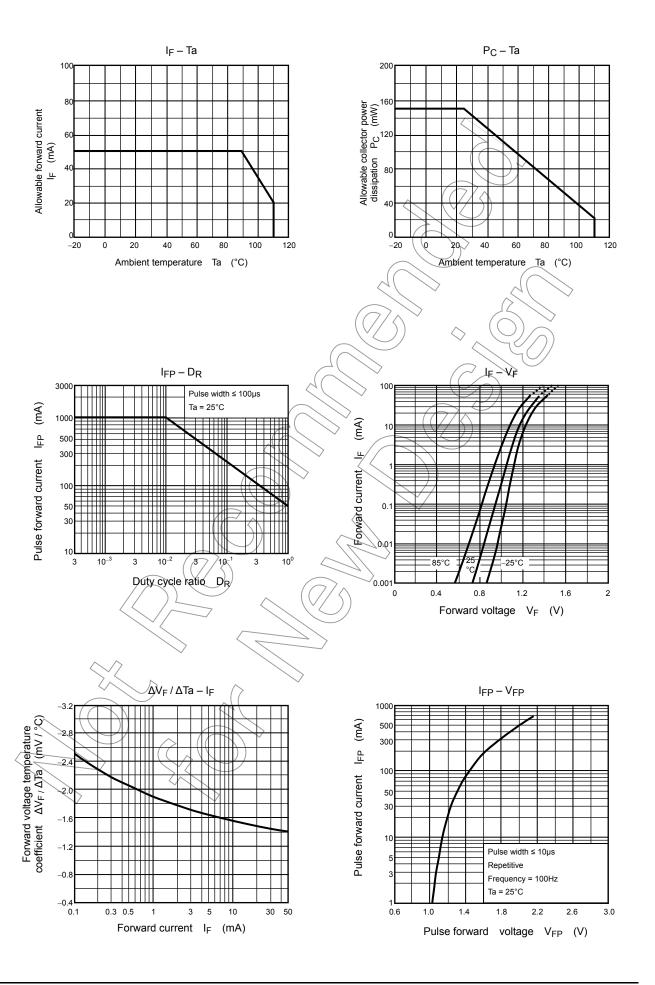
Switching Characteristics (Ta = 25°C)

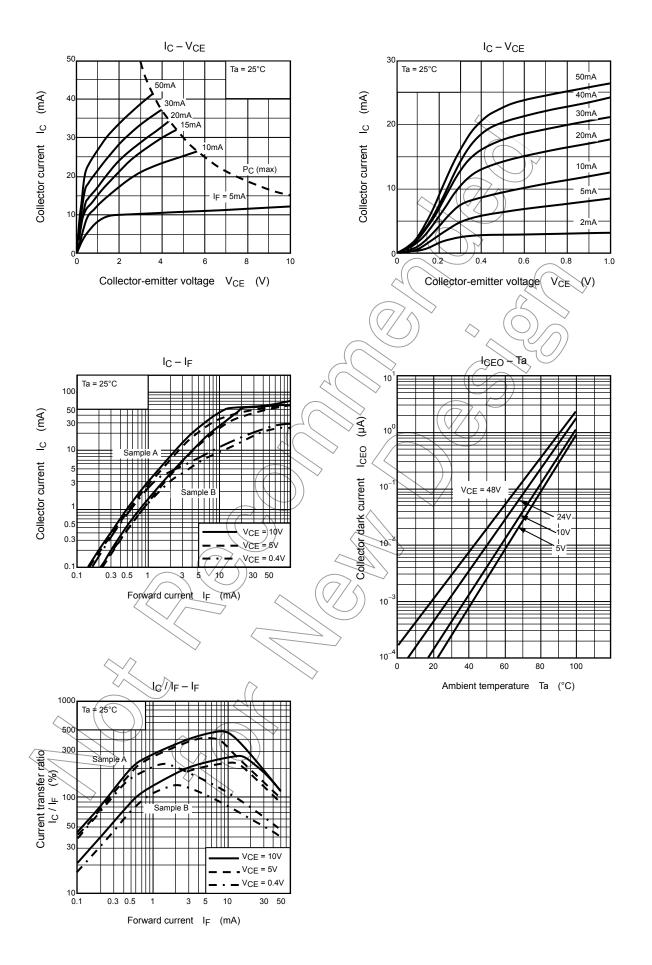
Characteristic	Symbol	Test Condition	Min	Тур.	Max	Unit
Rise time	t _r	$V_{CC} = 10 \text{ V, } I_{C} = 2 \text{ mA}$ $R_{L} = 100\Omega$	_	2	_	
Fall time	t _f		_	3	_	
Turn-on time	t _{on}			3	_	μs
Turn-off time	t _{off}			3	_	
Turn-on time	t _{ON}) > 2	_	
Storage time	t _s	$R_L = 1.9 \text{ k}\Omega$ (Fig.1) $V_{CC} = 5 \text{ V}, I_F = 16 \text{ mA}$	\nearrow	25	_	μs
Turn-off time	t _{OFF}))	40	_	

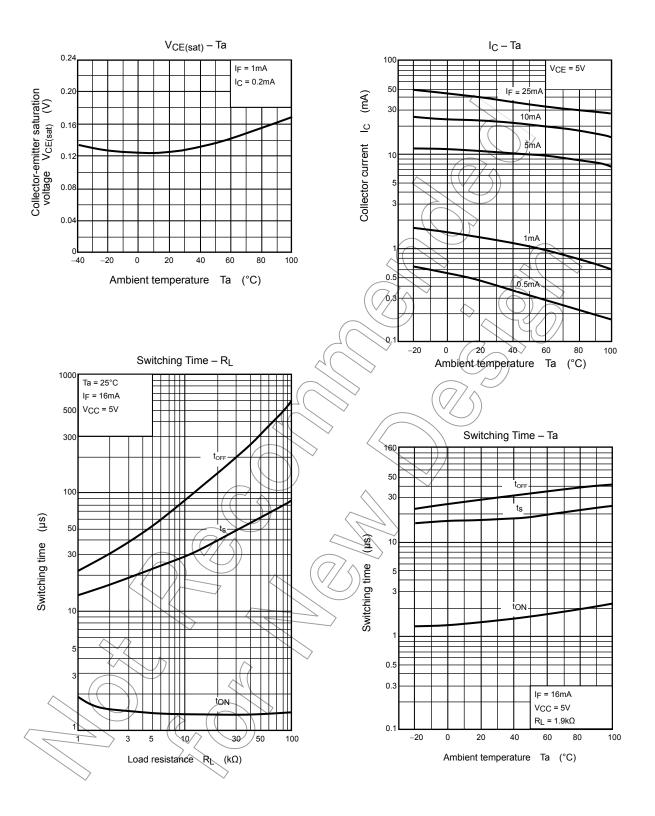
Fig. 1 Switching time test circuit



5 2009-11-12







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